

Course Details											
Code				Aca	Academic Year				Semester		
ETE101				2	2			3			
Title				т		Α	L	ECTS			
Digital Design								1	1	6	
Language	German	German									
Level	Undergraduate	Undergraduate X Graduate Postgraduate									
Department / Program	Materials Science	Materials Science and Technology									
Forms of Teaching and Learning	Face to face	Face to face									
Course Type	Compulsory					Elec	Elective			x	
Objectives	In this lecture stu syntheses and min	In this lecture students will learn mathematical foundations of digital systems, analyz syntheses and minimizations.					ms,analyzes,				
Content	Number systems, comparators, adde arithmetic logic ur	Number systems, Boolean algebra, minimization methods, MUX, DEMUX, comparators, adders, enhancers, extractors, multipliers, BarrelShift, arithmetic logic unit, memory units, register, counter, RAM									
Prerequisites											
Coordinator											
Lecturer(s)	Asist Prof.Dr. M. G	Asist Prof.Dr. M. Gökhan Habiboğlu									
Assistant(s)											
Work Placement	No No										
Recommended or Required Reading											
Books / Lecture Notes	Grundlagen der Techr	rundlagen der Technischen Informatik, dirk w. Hoffmann, Hanser, 2007									
Other Sources											
Additional Course Material											
Documents											
Assignments											
Exams											
Course Composition											
Mathematics und Basic Sciences	20%										
Engineering	20%										
Engineering Design	%										
Social Sciences	%										
Educational Sciences	%										



Natural Sciences				100%		
Health Sciences				%		
Expert Knowledg	ge		60%			
Assessment						
Activit	y		Percentage (%)			
Midterm Exam			1	30		
Quiz						
Assignments						
Attendance						
Recitations			14	20		
Projects						
Final Exam			50			
			100			
ECTS Points and	d Work Load	ł				
Activit	у	Count	Duration	Work Load (Hours)		
Lectures		14	2	28		
Self-Study		14	3	42		
Assignments		5	15	75		
Presentation / Seminar Preparation						
Midterm Exam		1	2	2		
Recitations		14	1	14		
Laboratory		14	2	28		
Projects						
Final Exam		1	2	2		
			191			
ECTS Points (Total Work Load / Hours) 6						
Learning Outcomes						
1	The bases of digital design and computer engineering					
2	Gaining the ability to operate in different types of algebra					
3	Gaining the ability to perform transactions in different number systems					
4	the ability to express logic operators, bool functions and logic formulas in different formations					
5	Minimization in circuit design, KV-Diagram solving					
6	Creation of basic circuits such as MUX, DEMUX, comparator, adder					
7	Design of digital storage units with simple logic circuits					
8	Access to Register and RAM from storage units					



9								
10								
11								
12								
Weekly Conter	nt							
1	Number sys	stems						
2	Bool Algebr	a 1						
3	Bool Algebr	a 2						
4	Minimizatio	on 1						
5	Minimizatio	on 2						
6	MUX, DEMUX, Comparators							
7	Collectors, enhancers, extractors							
8	Multipliers, Barrel-Shift, Arithmetic Logic Unit							
9	Memory units 1							
10	Memory units 2							
11	Register							
12	Counter							
13	RAM							
14								
15								
Contribution of Learning Outcomes to Program Objectives (1-5)								
	P1	P2	P3	P4	P5	P6	P7	P8
1	5	5	5	5	5	5	5	5
2	5	5	5	5	5	5	5	5
3	5	5	5	5	5	5	5	5
4	5	5	5	5	5	5	5	5
5	5	5	5	5	5	5	5	5
6	5	5	5	5	5	5	5	5
/	5	5	5	5	5	5	5	5
8	5	5	5	5	5	5	5	5
9								
11								
12								
Contribution Level 1: Low 2: Low-intermediate 3: Intermediate 4: High 5: Very High								



Compiled by:	
Date of Compilation:	